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This listing of claims will replace all prior versions, and listings, of claims in the application:

LISTING OF CLAIMS:

Claim 1 (previously presented): An oscillator device comprising:
an oscillation circuit including an NPN oscillation transistor and a buffer amplifier circuit including a PNP buffer amplifier transistor; wherein
the NPN oscillation transistor and the PNP buffer amplifier transistor are connected in series to a power supply;
a collector of the NPN oscillation transistor is connected to a power terminal and is grounded via a capacitor;
a base of the PNP buffer amplifier transistor is grounded via a capacitor;
at least one of a resistor and an inductor is connected between a collector of the PNP buffer amplifier transistor and the ground;
the collector of the PNP buffer amplifier transistor is AC-connected to an output terminal; and
an emitter of the NPN oscillation transistor and an emitter of the PNP buffer amplifier transistor are directly connected.

Claim 2 (original): The oscillator device according to Claim 1, further comprising at least two resistors connected at at least two positions respectively selected from between the power terminal and a base of the NPN oscillation transistor, between the base of the NPN oscillation transistor and a base of the PNP buffer amplifier transistor, and between the base of the PNP buffer amplifier transistor and a ground, such that individual bias voltages are applied to the base of the NPN oscillation transistor and the base of the PNP buffer amplifier transistor.

Claim 3 (original): The oscillator device according to Claim 1, wherein the

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oscillation circuit comprises a crystal oscillator connected between a base of the NPN oscillation transistor and a ground, and a capacitance element connected to the crystal oscillator, and the oscillation frequency varies by changing the value of the capacitance element.

Claim 4 (original): The oscillator device according to Claim 3, wherein the oscillator device is a temperature-compensated crystal oscillator device.

Claim 5 (original): The oscillator device according to Claim 1, wherein the NPN oscillation transistor and the PNP buffer amplifier transistor are integrated in a single package.

Claim 6 (previously presented): An oscillator device comprising:
an oscillation circuit including a PNP oscillation transistor and a buffer amplifier circuit including an NPN buffer amplifier transistor; wherein
the PNP oscillation transistor and the NPN buffer amplifier transistor are connected in series to a power supply;
a collector of the PNP oscillation transistor is connected to a power terminal and is grounded via a capacitor;
a base of the NPN buffer amplifier transistor is grounded via a capacitor;
at least one of a resistor and an inductor is connected between a collector of the NPN buffer amplifier transistor and the ground;
the collector of the NPN buffer amplifier transistor is AC-connected to an output terminal; and
an emitter of the PNP oscillation transistor and an emitter of the NPN buffer amplifier transistor are directly connected.

Claim 7 (original): The oscillator device according to Claim 6, further comprising

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at least two resistors connected at at least two positions respectively selected from between the power terminal and a base of the PNP oscillation transistor, between a base of the PNP oscillation transistor and a base of the NPN buffer amplifier transistor, and between the base of the NPN buffer amplifier transistor and a ground, such that individual bias voltages are applied to the base of the PNP oscillation transistor and the base of the NPN buffer amplifier transistor.

Claim 8 (original): The oscillator device according to Claim 6, wherein the oscillation circuit comprises a crystal oscillator connected between a base of the PNP oscillation transistor and a ground, and a capacitance element connected to the crystal oscillator, and the oscillation frequency varies by changing the value of the capacitance element.

Claim 9 (original): The oscillator device according to Claim 8, wherein the oscillator device is a temperature-compensated crystal oscillator device.

Claim 10 (original): The oscillator device according to Claim 6, wherein the PNP oscillation transistor and the NPN buffer amplifier transistor are integrated in a single package.

Claims 11-16 (canceled).

Claim 17. (original) An electronic apparatus comprising the oscillator device according to Claim 1.

Claim 18. (original) An electronic apparatus comprising the oscillator device according to Claim 6.

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Claim 19 (canceled).